

Amendment to the Claims:

This listing of claims will replace all prior versions, and listing of claims in the application.

Listing of Claims:

1. (Currently Amended) Optical disk system comprising at least one photo detector comprising several sub-detectors for detecting at least a part of said optical disk and in response, said at least one photo detector generating detection signals in response to said detection, the optical disk system further and comprising several circuits, each circuit having an input directly coupled to a respective output of one of said several sub-detectors for receiving said detection signals, said several circuits comprised of at least one amplifier for amplifying detection signals and comprising at least one slicer for slicing amplified detection signals ~~and, the system further~~ comprising at least one delay-difference detector for detecting delay differences in sliced amplified detection signals, characterized in that said delay-difference detector is delaylineless and comprises combinatorial-logic circuits and sequential-logic circuits.
2. (Original) Optical disk system according to claim 1, characterized in that said delay-difference detector comprises a first pair of sequential-logic circuits for detecting delay differences between rising edges and comprises a second pair of sequential-logic circuits for detecting delay differences between falling edges.
3. (Original) Optical disk system according to claim 2, characterized in that said delay-difference detector further comprises at least one analog adder/subtractor for adding/subtracting sequential-logic circuit output signals.

4. (Original) Optical disk system according to claim 3, characterized in that said delay-difference detector comprises at least one low pass filter coupled to an output of said at least one analog adder/subtractor.

5. (Original) Optical disk system according to claim 3, characterized in that said delay-difference detector comprises at least one low pass filter located between at least one sequential-logic circuit and said at least one analog adder/subtractor.

6. (Original) Delay-difference detector for use in an optical disk system comprising at least one photo detector for detecting at least a part of said optical disk and in response generating detection signals and comprising at least one amplifier for amplifying detection signals and comprising at least one slicer for slicing amplified detection signals and comprising at least one delay-difference detector for detecting delay differences in sliced amplified detection signals, characterized in that said delay-difference detector is delaylineless and comprises combinatorial-logic circuits and sequential-logic circuits.

7. (Original) Delay-difference detector according to claim 6, characterized in that said delay-difference detector comprises a first pair of sequential-logic circuits for detecting delay differences between rising edges and comprises a second pair of sequential-logic circuits for detecting delay differences between falling edges.

8. (Original) Delay-difference detector according to claim 7, characterized in that said delay-difference detector further comprises at least one analog adder/subtractor for adding/subtracting sequential-logic circuit output signals.

9. (Currently Amended) A Method method for use in an optical disk system, and comprising the steps of:

detecting at least a part of said optical disk using at least one photo detector comprised of at least several sub-detectors; and of in response

generating detection signals from said at least several sub-detectors, responsive to said detection; and of

independently amplifying said detection signals from each of said at least several sub-detectors to generate amplified detection signals; and of

slicing said amplified detection signals to generate sliced amplified detection signals; and of

detecting delay differences in said sliced amplified detection signals; wherein characterized in that said detecting step of detecting delay differences is delaylineless and comprises the substeps of combinatorial logic circuiting and sequential logic circuiting.

10. (Original) Method according to claim 9, characterized in that said step of detecting delay differences comprises the substeps of detecting delay differences between rising edges and of detecting delay differences between falling edges.

11. (Cancelled)